

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re

U.S. application of: Yoshio HAGIHARA and Kenji TAKADA

For: IMAGE-SENSING APPARATUS

U.S. Serial No.: To Be Assigned

Filed: Concurrently

Group Art Unit: To Be Assigned

Examiner: To Be Assigned

BOX PATENT APPLICATION
Assistant Director
for Patents

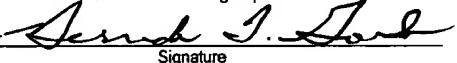
Washington, D.C. 20231

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July 19, 2000
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INFORMATION DISCLOSURE STATEMENT

In compliance with the duty of disclosure set forth in 37 C.F.R. § 1.56, Applicants wish to bring the following items to the attention of the Examiner. A copy of each document is enclosed for the convenience of the Examiner.

No representation is made, and no representation is intended, that more relevant material does not exist, or that the order of presentation of this material in any way reflects its relative pertinence. The references cited below are not intended to constitute an admission of any kind. Specifically, this presentation is not an admission

Attorney Docket No. 15162/02280

that the items listed below are properly citable against the above-identified application.

The following document is cited in the specification of the above-identified patent application:

- (1) Miyatake et al., U.S. Patent No. 5,241,575, issued August 31, 1993.

Applicants also wish to bring the following documents to the attention of the Examiner:

- (2) Nakamura et al., U.S. Patent No. 5,289,286, issued February 22, 1994;
- (3) M. Loose et al., "Self-Calibrating Logarithmic CMOS Image Sensor With Single Chip Camera Functionality", Heidelberg University Institute of High Energy Physics (IHEP), pp. 191-194; and
- (4) S. Kavadias et al., "On-Chip Offset Calibrated Logarithmic Response Image Sensor", IMEC, pp. 68-71.

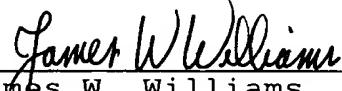
Applicants consider the invention to be distinguishable over the above-cited documents.

As this Information Disclosure Statement is being filed concurrently with the application, no fee is incurred. However, if it should be determined that a fee is required, please charge any required fee (other than the issue fee) during the pendency of this application to

Attorney Docket No. 15162/02280

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Respectfully submitted,



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July 19, 2000

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Substitute for form 1449A/PTO				Complete if Known				
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	To Be Assigned			
				Filing Date	Concurrently			
				First Inventor	Yoshio HAGIHARA et al.			
				Group Art Unit	To Be Assigned			
				Examiner Name	To Be Assigned			
Sheet	1	Of	1	Attorney Docket No. 15162/02280				

U.S. PATENT DOCUMENTS

Examiner Initials	Cite #	DOCUMENT NUMBER	C O D E	PATENTEE	ISSUE DATE (mm/dd/yy)	CLASS	SUB CLASS	Filing Date if Appropriate
	(1)	5,241,575		Miyatake et al.	08/31/93	377	60	
	(2)	5,289,286		Nakamura et al.	02/22/94	348	223	

FOREIGN PATENT DOCUMENTS

Examiner Initials	O F F I C E	NUMBER	C O D E	PUBLICATION DATE (mm/dd/yy)	TRANSLATION Yes No

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	(3)	M. Loose et al., "Self-Calibrating Logarithmic CMOS Image Sensor With Single Chip Camera Functionality", Heidelberg University Institute of High Energy Physics (IHEP), pp. 191-194
	(4)	S. Kavadias et al., "On-Chip Offset Calibrated Logarithmic Response Image Sensor", IMEC, pp. 68-71
Examiner Signature		Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with the next communication to applicant.